

Sr. No	Name of the Programme	Name of Course	Duration of the Course	Sanctioned Intake	Eligibility Conditions
1.	M. Tech.	Electronics & Comm. Engg.(VLSI Design)	2 Years	18	B.E./B.Tech or equivalent in Electronics Engineering / Electronics & Instrumentation Control/ Instrumentation & Control/ Electronics & Communication Engg./Electronics & Telecommunication Engg./Electronics Instt. & Control/Electrical & Electronics Engg./ Instrumentation Engg./M.Sc. (Electronics) scoring at least 50% (47.50% for SC/PH) marks in aggregate.