Sr. No	Name of the Programme	Name of Course	Duration of the Course	Sanctioned Intake	Eligibility Conditions& Career Scope
1.	M.Tech	Electronics & Communication Engineering with specialization in Embedded System & VLSI	2 Years	6	Minimum 50% marks or equivalent CGPA in B.E./B.Tech in Electronics & Communication Engg./ Telecommunication Engg./ Computer Science & Engineering/ Instrumentation & Control Engg./ Electrical & Electronics Engg./ Electrical Engg./ Power Electronics or/ MCA or M.Sc Computer Science/ Information Technology/ Electronics/Physics/ Mathematics as one of the subjects. **Career opportunities**: India dominates in the back-end part of VLSI i.e. Physical Design (actual implementation of the chip) and Verification – writing tests, measuring coverage, etc. Over 150 international companies are catering to this industry, like Texas Instruments, Infineon, Free scale Semiconductor, Cadence, HCL, Intel, Lucent, Motorola, Philips Semiconductor, Qualcomm, Sasken, Conexant, Wipro, STMicroelectronics and TCS, to name a few which does bulk of the work and employs a lot of young VLSI engineers.

					VLSI will improve the cost-effectiveness of medical devices through advanced, smaller-sized circuitry, which provides increased sophistication and enhanced function. The market is moving towards digitization and there will be a huge demand for the trained Electronics Engineer Who will be able to fulfill the demands of these designs. The Emerging growing areas that will require lots of chips are Machine learning, IoT for monitoring small devices, and surveillance. ML requires design of large chips, whereas IoT requires miniature designs, Thus there is a whole spectrum of work, from big chips to small chips to everything in between. There is exponential rise in the job market for a VLSI design or testing Engineer, Communication and Automobile Sector along with Biomedical and Health Care. In the VLSI chip design and verification alone, 20,000 to 30,000 engineers are currently employed with over 200,000 engineers working in the broader semiconductor industry, including embedded systems development and board-level hardware design.